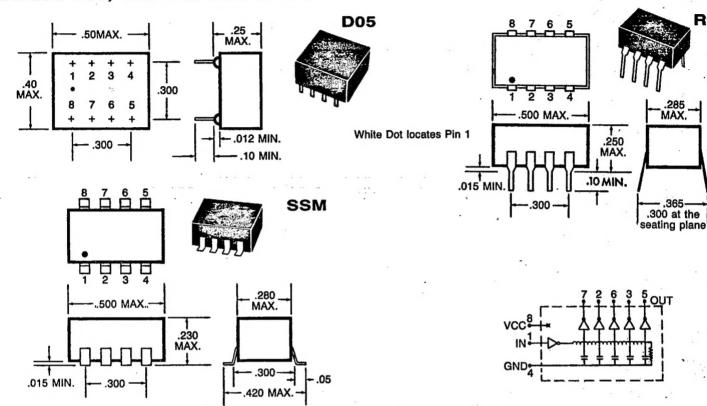
**R05** 

.285 MAX,



## **DIL-DIP AND SURFACE MOUNTING DIGITAL DELAY LINES** TTL COMPATIBLE **8 PIN PACKAGE**

## SERIES DO5, R05 AND SSM-5TAPS



A Section 1	MODEL NO.	New York Control	TOTAL	DELAY
SERIES DOS	SERIES R05	SERIES SSM	(ns)	(ns)
D05025	R05025	\$SM-05025	25	5
D05030	R05030	SSM-05030	- 30	···· 6, ·:
D05040	R05040	SSM-05040	40	8
D05045	R05045	SSM-05045	45	9
D05050	R05050	SSM-05050	50	10
D05075	R05075	SSM-05075	75	15
D05100	R05100	SSM-05100	100	.20
D05125	R05125	SSM-05125	125	25
D05150	R05150	SSM-05150	150	30
D05200	R05200	SSM-05200	200	40
D05250	R05250	SSM-05250	250	50
D05300	R05300	SSM-05300	300	60
D05400	R05400	SSM-05400	400	80
D05500	R05500	SSM-05500	500	100

		LIMITS		
-, DC F	ARAMETERS	Min.	Max.	
Voh	Vcc = min loh = 1.0mA	2.5V		
Vol	Vcc = min lol = 20mA	-	0.5V	
lih	Vcc = max Vih = 2.7V	_	50μA	
lil	Vcc=max Vii=0.5V	-2.0mA	-	
li	Vcc = max Vi = 5.5V	-	1.0mA	
Vi	Vcc = min lin = - 18 mode	- 1.2vdc	-	
lcc	Vcc = max outputs low	-	70mA	

## SPECIFICATIONS:

· Supply voltage: 5.0VDC ± 10% Delay tolerances: ±2ns or ±5% wig · Minimum pulse width: 40% of Total Delay

 Maximum duty cycle: 50% · Rise time:

4ns max

• Operating temp. range: 0°C to +70°C • Temp. coeff. of delays: 1.0ns +500ppm/°C

· Terminals:

.020w x .010th., alloy 42

## **TEST CONDITIONS:**

- Vcc = 5.0VDC, Temp. 25°C ± 5°C
- · Time delay measured at the 1.5V level
- · Rise time measured from .75V to 2.4V
- · All outputs loaded with 15pf
- Input Test Pulse:

Pulse Voltage: 3.0V Pulse rise time: 2ns

Pulse width: 1.2 x max Td Pulse spacing: 5 x max Td